Fig. 1

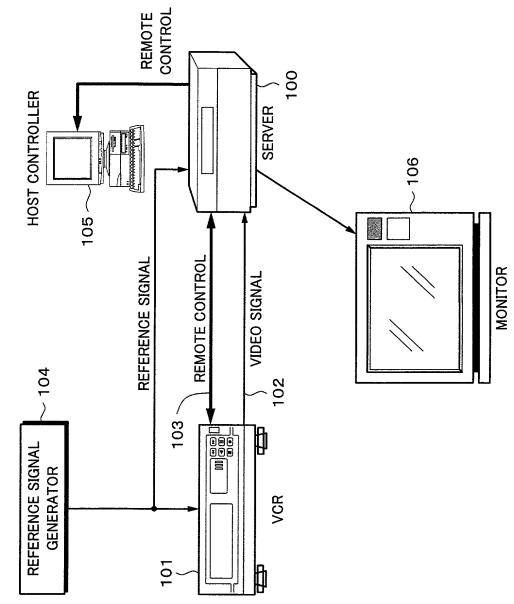


Fig. 2

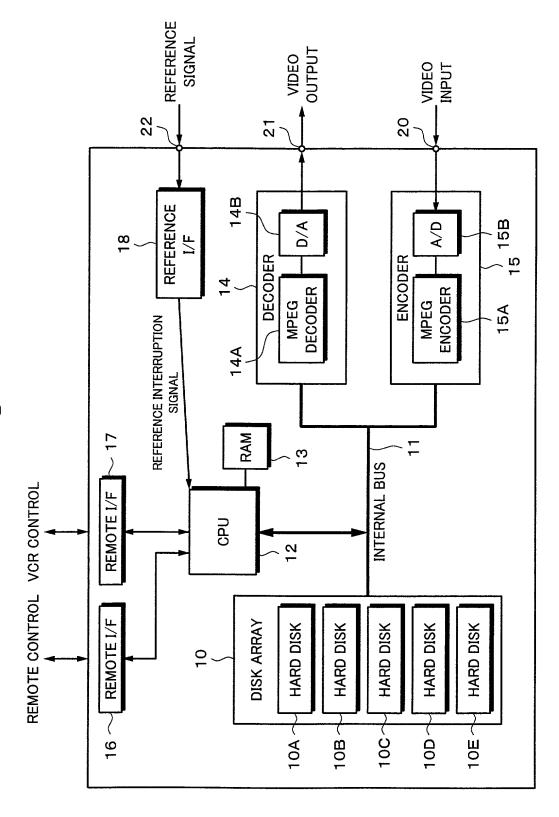
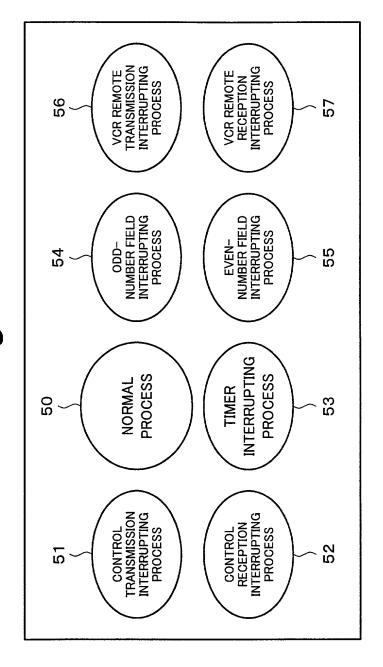


Fig. 3



PROCESSES OF CPU

VERTICAL BLANKING INTERVAL EVEN INTERUPTION VERTICAL BLANKING INTERVAL ODD INTERUPTION VIDEO SIGNAL VERTICAL BLANKING INTERVAL EVEN INTERUPTION **VERTICAL BLANKING** INTERVAL VERTICAL BLANKING INTERVAL ODD INTERUPTION Fig. 4B Fig. 4C

Fig. 4A

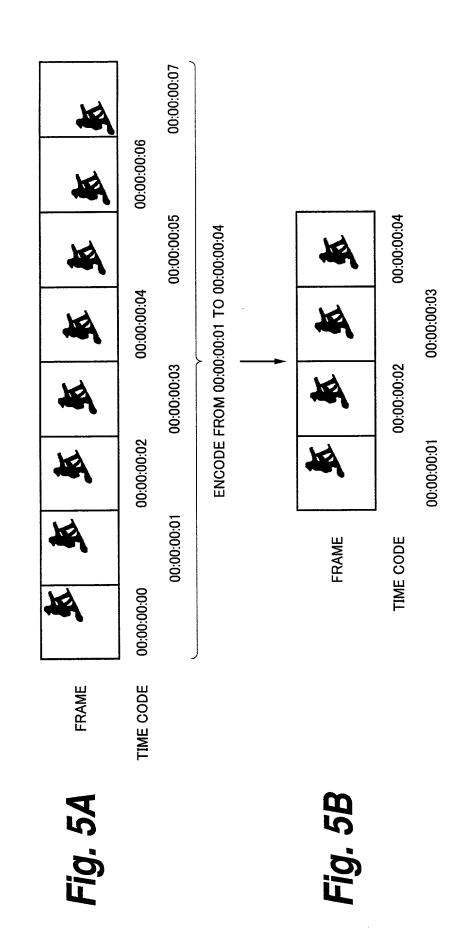


Fig. 6

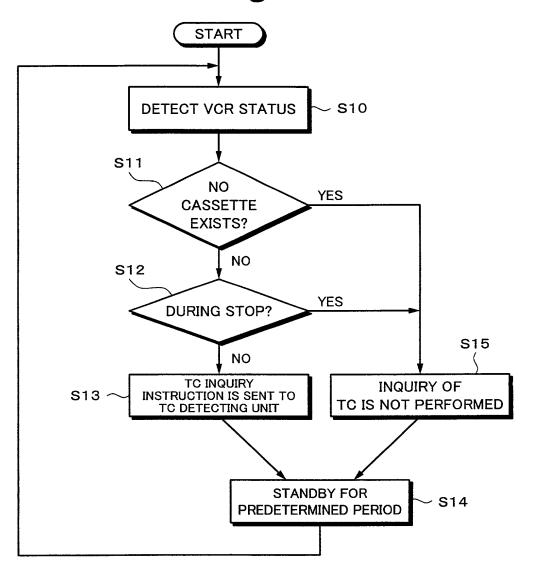


Fig. 7A

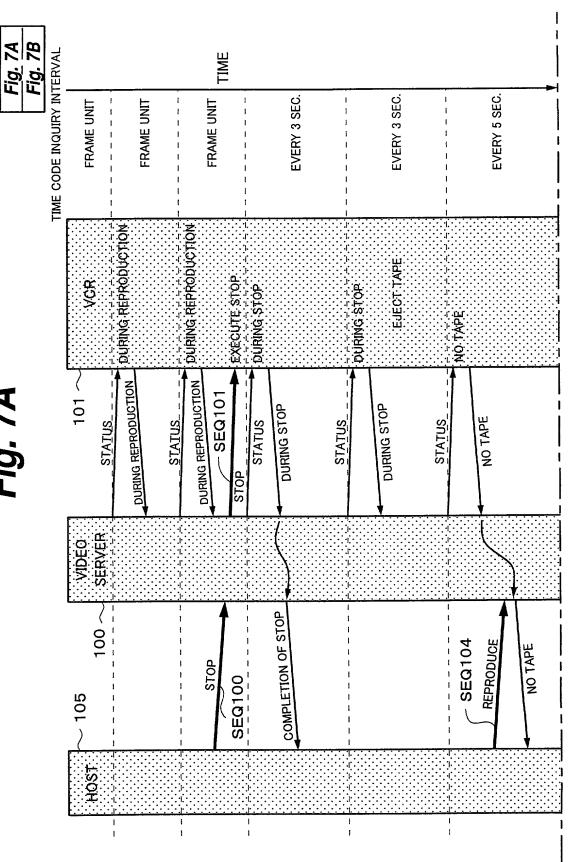


Fig. 7B

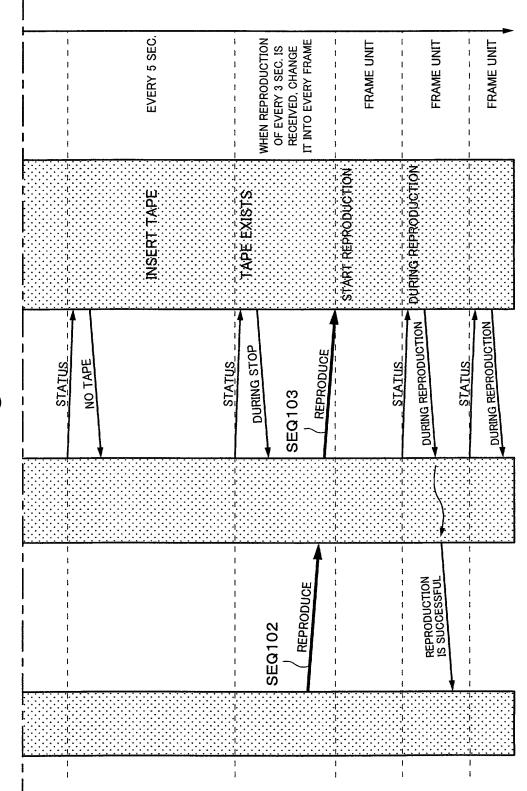


Fig. 8

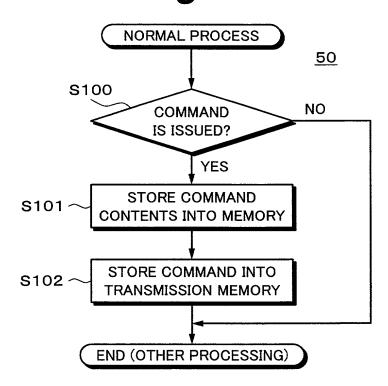


Fig. 9

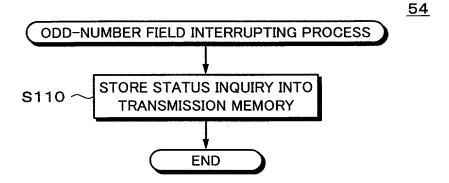


Fig. 10

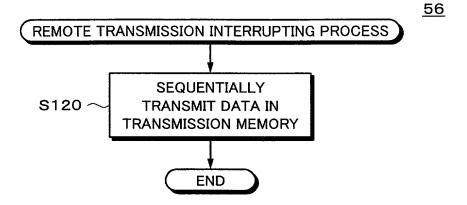


Fig. 11

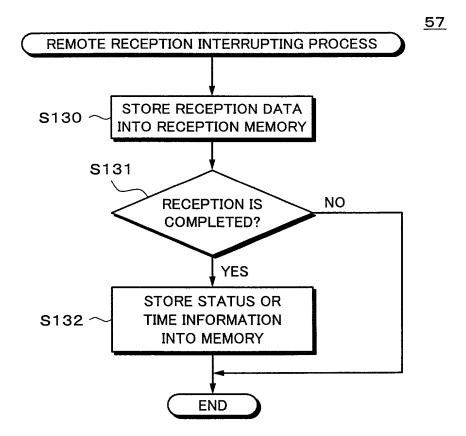


Fig. 12

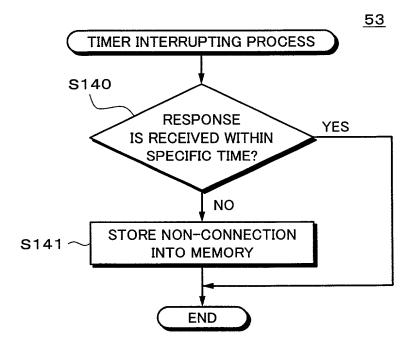


Fig. 13

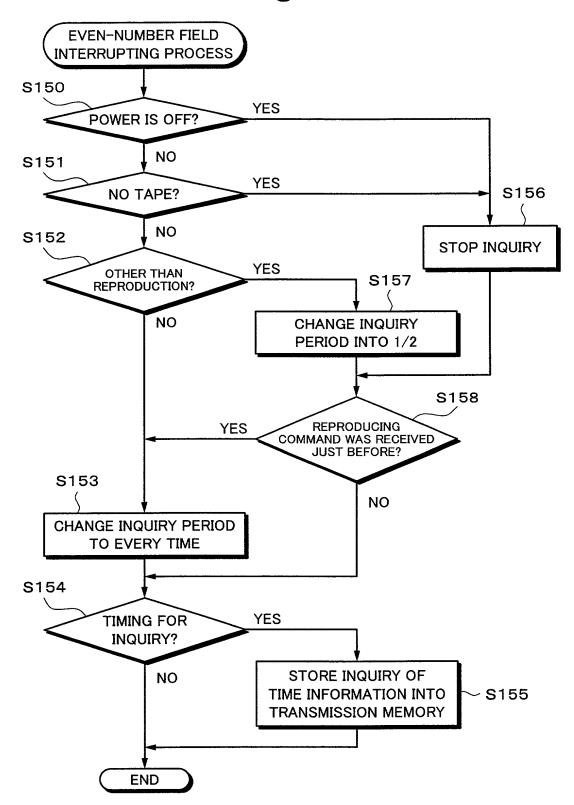
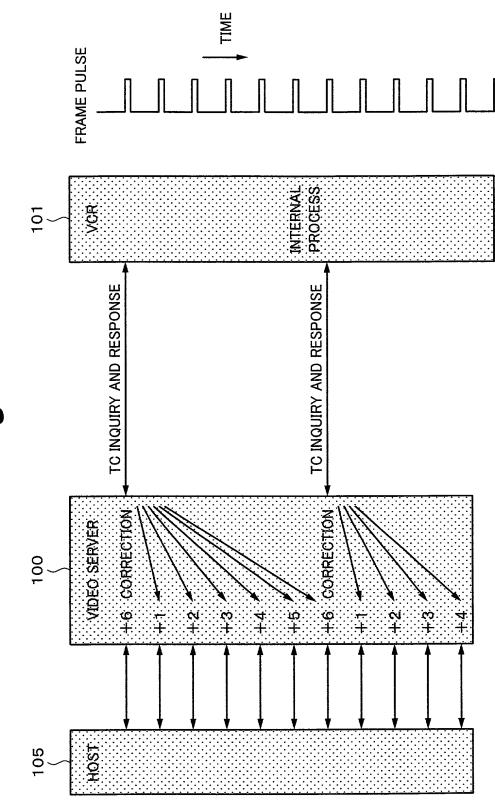


Fig. 14



ARITHMETIC OPERATION AND CORRECTION OF TIME CODE

Fig. 15

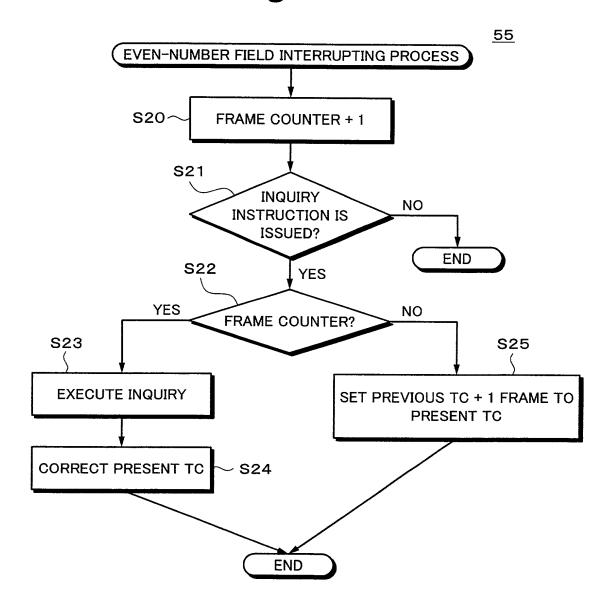


Fig. 16

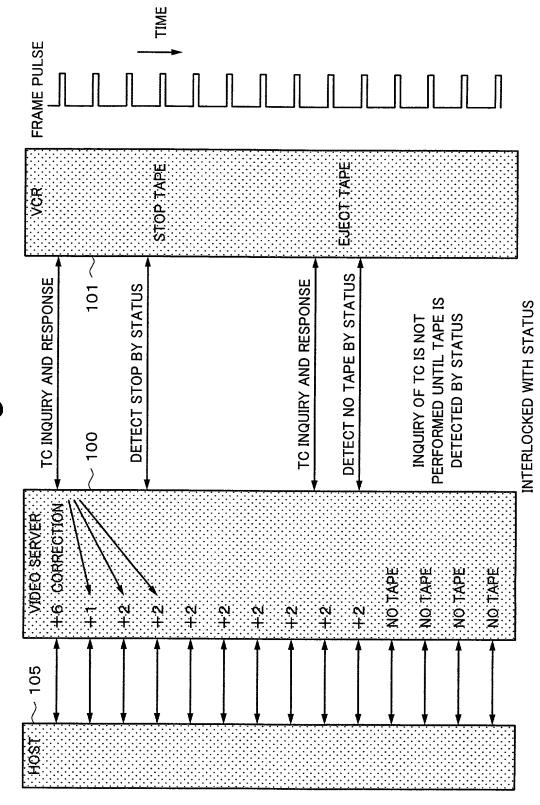


Fig. 17

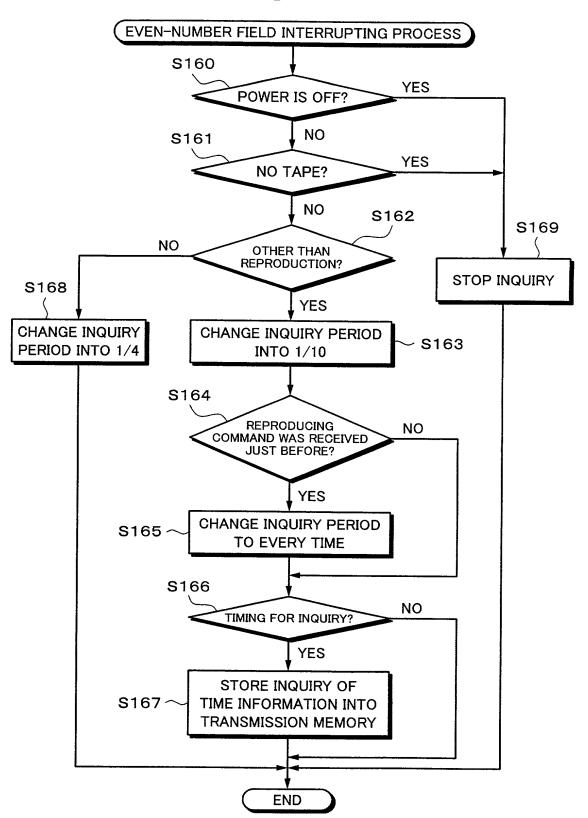


Fig. 18

